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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09 627,979	07 28 2000	Randy H. Y. Lo	UPA-00156	3057	
7	7590 12 03				
Jason Z Lin			EXAMINER		
19597 Via Mo Saratoga, GA			NGUYEN, DILINH P		
			ART UNIT	PAPER NUMBER	
			2814		

DATE MAILED: 12/03/2001

Please find below and/or attached an Office communication concerning this application or proceeding.

	,	Application No.	pplicant(s)				
·		09/627.979	LO ET AL	p /1_			
•	Office Action Summary	Examiner	Art Unit	- ("			
		DiLinh Nguyen	2814				
Period	The MAILING DATE of this communication a for Reply	ppears on the cover sheet	with the correspondence address	•			
TH - 6 2 - 1: - 1: - F - A	SHORTENED STATUTORY PERIOD FOR REP E MAILING DATE OF THIS COMMUNICATION xtensions of time may be available under the provisions of 37 CFR 1 fter SIX (6) MONTHS from the mailing date of this communication the period for reply specified above is less than thirty (30) days a re NO period for reply is specified above, the maximum statutory periodalure to reply within the set or extended period for reply will, by statuny reply received by the Office later than three months after the mailing armed patent term adjustment. See 37 CFR 1 704(b)	I.  1.136(a) In no event however may epply within the statutory minimum of id will apply and will expire SIX (6) N ute cause the application to become	thirt, 30, days will be considered timely toon to mail to mail to the mailing date of this communical ABANDONED (35 U.S.C. & 133)	tion			
1)[		3 July 2000 .					
2a)[		This action is non-final.					
3)[	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispos	sition of Claims						
4)[	☐ Claim(s) <u>1-26</u> is/are pending in the application	on.					
	4a) Of the above claim(s) is/are withdra	awn from consideration.					
5)[	Claim(s) is/are allowed.						
6)[	Claim(s) <u>1-26</u> is/are rejected.						
7)[	Claim(s) is/are objected to.						
8)[	Claim(s) are subject to restriction and/	or election requirement.					
Applic	ation Papers						
9)[	The specification is objected to by the Examin	er.					
10)	The drawing(s) filed on is/are: a)☐ acce	epted or b) objected to b	the Examiner				
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11)	The proposed drawing correction filed on	is: a)	disapproved by the Examiner.				
	If approved, corrected drawings are required in re	• •					
12)[	The oath or declaration is objected to by the E	xaminer.					
Priority	under 35 U.S.C. §§ 119 and 120						
13)[	Acknowledgment is made of a claim for foreig	gn priority under 35 U.S.C	:. § 119(a)-(d) or (f).				
	a) All b) Some * c) None of:						
	1. Certified copies of the priority documen	nts have been received.					
	2. Certified copies of the priority documen	nts have been received in	Application No				
,	3. Copies of the certified copies of the price application from the International Boundary See the attached detailed Office action for a list	ureau (PCT Rule 17.2(a)	),				
	Acknowledgment is made of a claim for domes	,		ition)			
	a) The translation of the foreign language pr Acknowledgment is made of a claim for domes	ovisional application has	been received.				
Attachm		nio priority under 55 0.5.1	5-33 120 and/or 121:				
1) 🔀 No 2) 🔲 No	tice of References Cited (PTO-892) tice of Draftsperson's Patent Drawing Review (PTO-948) primation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of	w Summary (PTO-413) Paper No(s) of Informal Patent Application (PTO-152)				

Application/Control Number: 09/627,979

Art Unit: 2814

### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.
- 2. Claims 1-3, 7, 11-13, 15-17, 21 and 25-26 are rejected under 35 U.S.C. 102(e) as being anticipated by Sota et al. (U.S. Pat. 6064111).

Sota et al. disclose a multi chip module package structure (figs. 2a –2f, column 9, lines 20-30) comprising :

a substrate (column 9, line 34);

a plurality of chip package bodies 29, wherein the chip package body is CSP (column 1, line 38) with a wire bonding;

a plurality of electrical gold wires 30, electrically connecting the chip package bodies and the substrate;

a plurality of electrical solder balls 32; and

a semiconductor sealing epoxy resin 31, enclosing the substrate, connect points and chip package body.

### Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

Art Unit: 2814

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

- 4. Claims 4, 6, 8, 10, 18, 20, 22 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sota et al. in view of Chiang et al. (U.S. Pat. 6307256).
- Regarding claims 6, 10, 20 and 24, Sota et al. disclose the claimed invention except for the chip package bodies pass burn-in test and function test. Chiang et al. disclose a semiconductor package comprising: a chip passes burn-in test and function test (column 5, lines 64-67) to ensure the maximum yield on the wafer. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Sota et al. to ensure the maximum yield on the wafer.
- Regarding claims 4, 8, 18 and 22, Chiang et al. disclose one of the chips is flip chip mounted or attached (column 6, lines 39-40).
- 5. Claims 5, 9, 14, 19 and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sota et al.

Sota et al. disclose the claimed invention except for at least one of the chips has a central pad bonding or bare chip. It would have been obvious to use the same technique for a chip with a central pad bonding or a bare chip. It would have been obvious to one having ordinary skill in the art at the time the invention was made to do so.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (703) 305-6983. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, OLIK CHAUDHURI can be reached on (703) 308-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

DLN

November 16, 2001

Douglas Wille

Patent Examiner

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